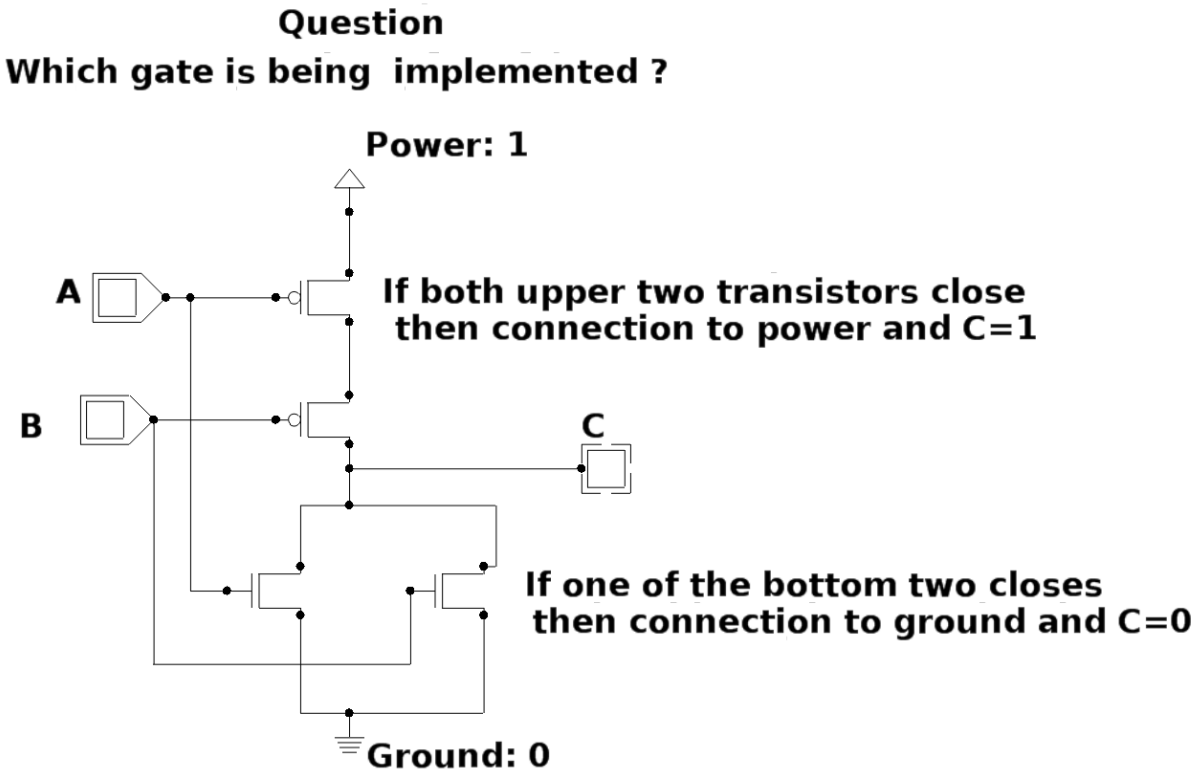
Examples: Circuits
These are the images of the circuits from the pages in Set1 (Cedar Logic).

- Page 1 is implementation of NAND gate and NOT gate (from notes)

- Notation:
- p-type transistor has circle at gate
  - p-type closes when input to gate is 0
- n-type transistor does not have circle at gate
  - n-type closes when input to gate is 1

Set 1, Page 2: What is the function being implemented by this circuit ?

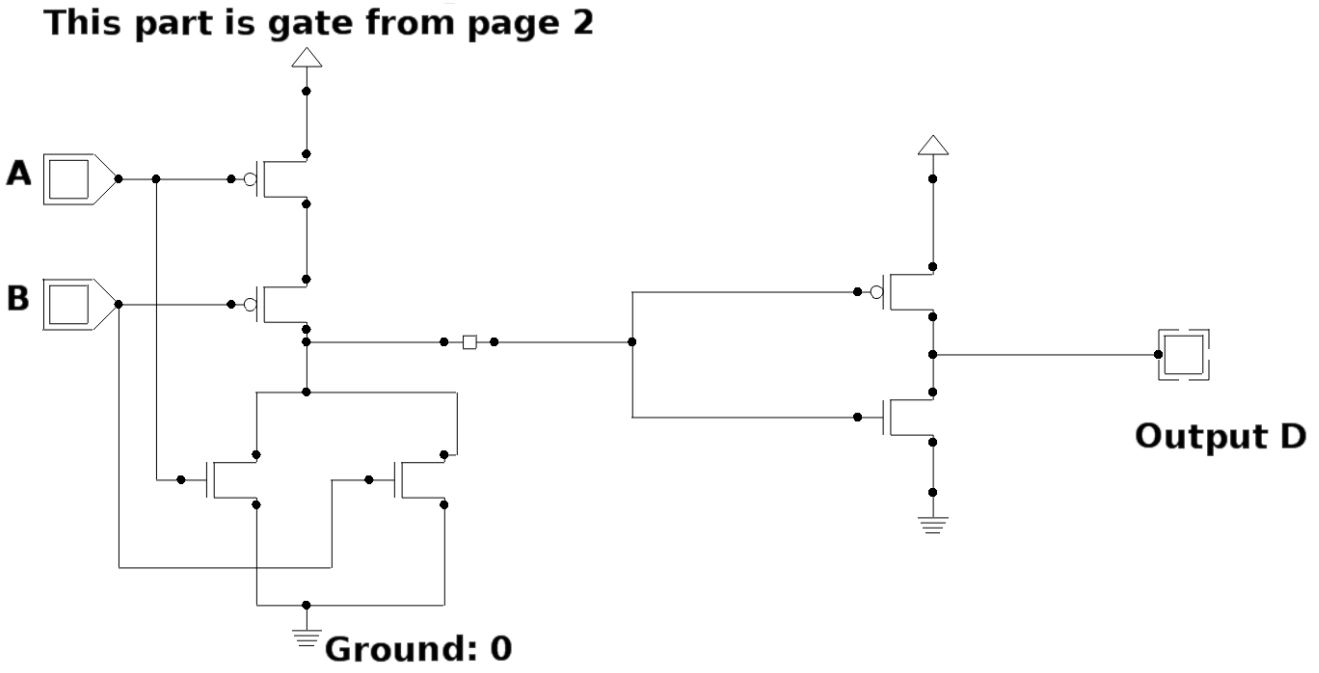
- Inputs A, B and Output is C. Derive truth table



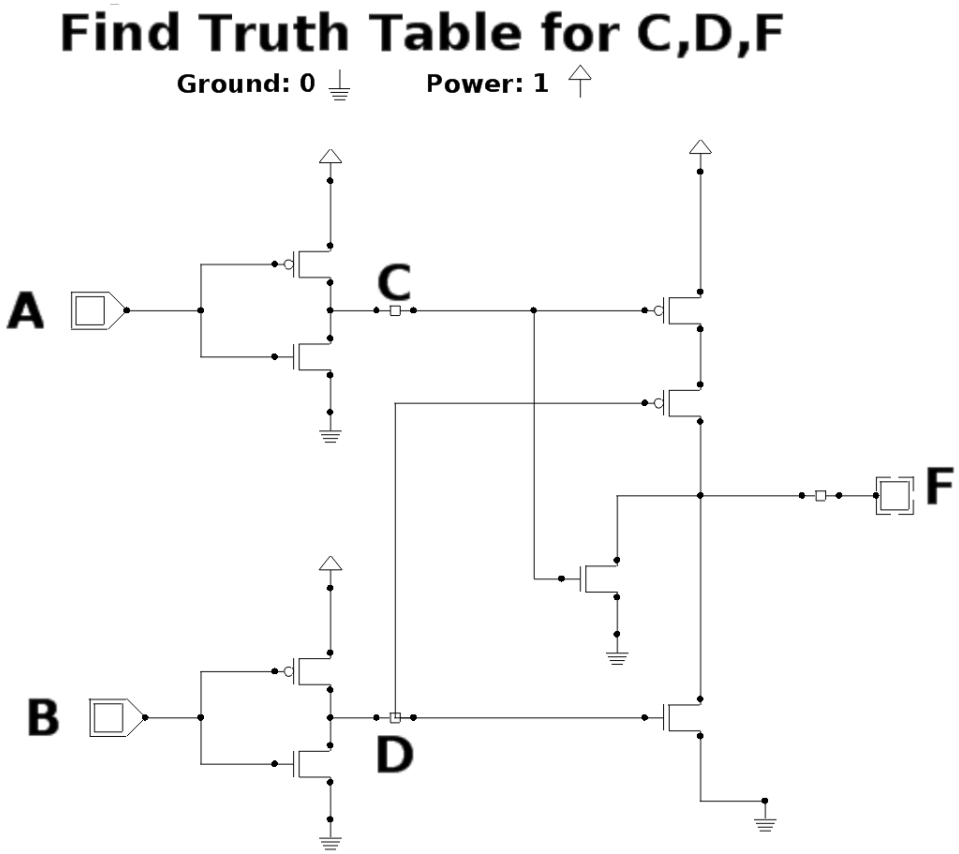
Set 1, Page 3: Combining ‘gates’.

Which gate is being implemented ?

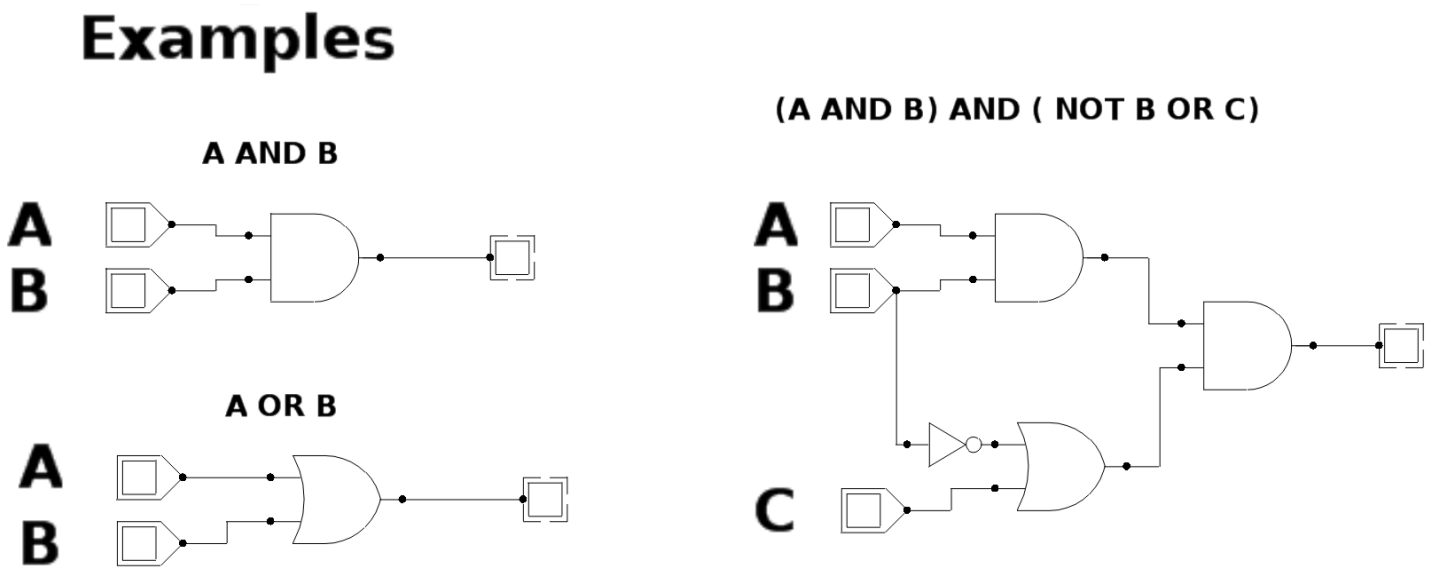
Combining gates to build new 'gates'



Set 1, Page 4: Inputs A,B. Find truth table and function F

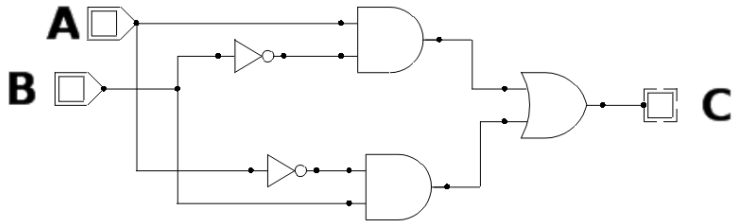


Set 1, Page 5: Examples of combinational circuits



Set 1, Page 6

(a) What is the boolean function for this circuit



(b) What is the boolean function F